

In-Circuit Emulator for MC68020/30

- 33 MHz no-waitstate emulation
- MMU support
- FPU support
- Support for companion mode MC68360
- Interface with all compilers
- C++ support
- CASE tools interface
- RTOS support
- Windows9x, NT and Motif interface

MC68020

MC68030

MC68EC020

MC68EC030

The ICE68020 emulator module supports the MC68020 and MC68030 processors from Freescale. The EC versions are supported as well.

The software has direct access to MMU and FPU functions.

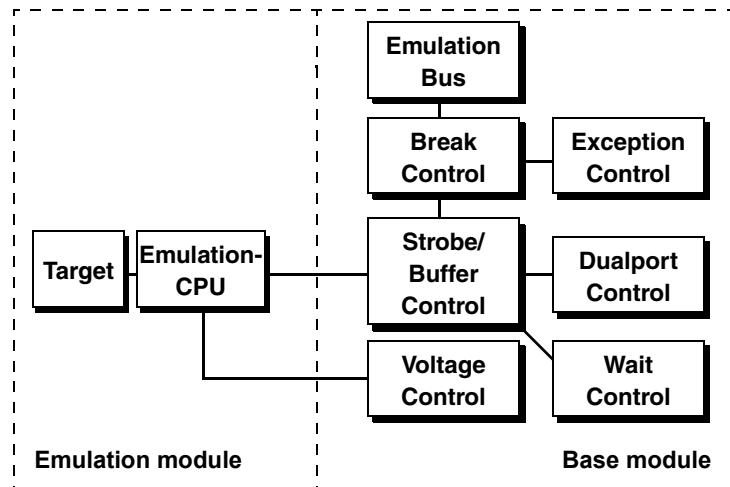
A software compatible ROM monitor is available.

TRACE32-ICE is a state of the art In-Circuit Emulator, which offers unlimited hardware breakpoints and up to 16MByte dual-ported emulation memory. The real-time trace and trigger work up to the max. speed of the CPU. The analyzer offers selective trace as well as performance analysis and statistic functions.

TRACE32 works with the highest variety of host interfaces. The communication link to the host is done by the printer port, a fibre optic interface or ethernet allowing a high-speed transfer.

Features

Basics of Operation



The ICE-68020 is a high-performance emulation system for all 68020 and 68030 derivatives from Motorola.

Operating Modes

The Emulator can work in stand-alone mode with internal clock or in active mode with internal or the target clock. On power-down of the target system the emulator tristates its output buffers and isolates its internal emulation circuits.

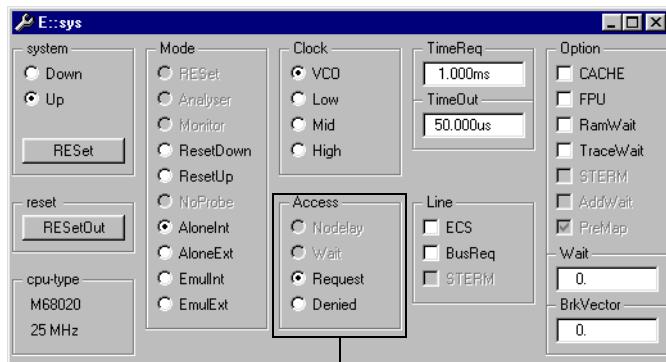
- Reset Down
- Reset Up
- No Probe
- Alone Internal
- Alone External
- Emulation Internal
- Emulation External

Wait System

- Additional wait cycles (1-15) can be specified
- Up to 250 wait cycles (4K blocks globally and bytewise in 64K area)

Voltage and Clock Monitors for the Target System

Dual-Port Access



Dual port access modes

All TRACE32 memories are dual-ported. The dual-port access makes it possible to display and modify the contents of the overlay memory, to set or delete breakpoints or use the flag memory while the application is running in real-time.

The following dual-port access mode is implemented:

- Request

In the **Request mode** the dual port access is always possible.

In the **Denied mode** the dual port access is switched off.

MMU Support



- Display of Register
- Display of translation tables
- Debugging with logical addresses
- Separate logical memory spaces for multitask environments

Emulation does not use any Memory

- No target stack required

Cache Support

ICE-68020 supports cache operations for the target memory.

HLL Debugging

addr/line	code	label	mnemonic	comment
555 {				
SP:00001788 48E73830	sieve:	movem.l	[d2-d4,a2-a3],-(a7)	
SP:0000178C 227C00002774		movea.l	#2774,a1 ; #flags,a1	
	register int i, prime, k;			
	int count;			
559		count = 0;		
SP:00001792 7200		moveq	#0,d1 ; #0,count	
561		for (i = 0 ; i <= SIZE ; flag		Add to Watch Window
SP:00001794 7400		moveq	#0	View in Window
SP:00001796 2042		movea.l	d2	Modify Value
SP:00001798 5282		addq.l	#1	Display Memory...
SP:0000179A 13BC000018800		move.b	#1	Breakpoints...
SP:000017A0 7012		moveq	#1	Go Till...
SP:000017A2 B082		cmp.l	d2	0
SP:000017A4 6CF0		bge	17	other...

The TRACE32 debugger provides a interface to C, C++, PASCAL, PEARL and ADA for all standard compilers. For more information on the supported compilers refer to the section "Debug Interfaces".

Full support in real-time for:

- Break-before-line operation
- HLL single step in real-time
- Trigger and trace on local variables

Background Task

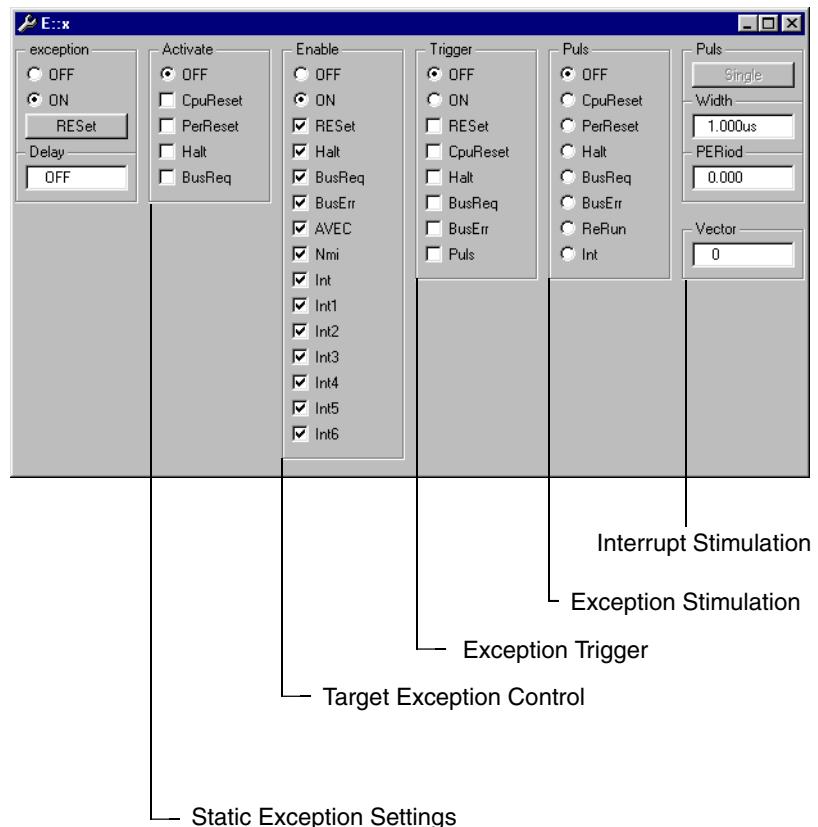
The background task allows:

- To process interrupts

- To maintain the refresh of a target DRAM

while the emulation is stopped.

Exception Control



The TRACE32 exception controller allows to permanently activate an exception, to enable or disable specific exceptions, to trigger on specific exceptions or to stimulate an exception.

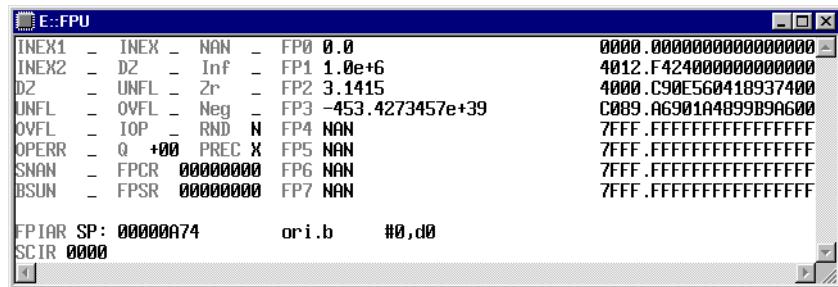
- Interrupt Simulation
 - Level-1...7-interrupts
 - All vectors

TRAPs used for Emulation

Program breakpoints are installed by a BPT instruction. ICE-68040 offers free selection of the breakpoint vector to avoid conflicts with other applications.

- BPT0,2,4,7 selectable

FPU Support



The display and the modification of the FPU registers is supported.

32 Additional Trace Channels

- 16 Bit Date 4 Bit State
- 12 Bit Address

Limitations

- No Burst Mode (68030)

Emulation Modules

Modules Overview

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Debug Interfaces

TRACE32-PowerView supports most compilers, realtime operation systems and debuggers.

New integrations are mostly done on customers request. If your compiler or RTOS is not supported now, please ask us !

Compiler Support

Language	Compiler	Company	Option	Comment
ADA	ALSYS-ADA		IEEE	limited support (IEEE)
ADA	TELESOFT-ADA	Telesoft	IEEE	limited support (IEEE)
ASM	RTOS	IEP GmbH	SYM/LOC	Source level debugging
ASM	ASM68K	Mentor Graphics Corporation	IEEE	Source level debugging
ASM	VERSADOS-ASM	NXP Semiconductors	VERSAD OS	symbols only
ASM	OS-9-ASSEMBLER	Radisys Inc.	ROF	Source level debugging
ASM	AS68	TASKING	IEEE	
C	HP-64000-C		HP	no type/locals info
C	ORGANON	CAD-UL ElectronicServices GmbH	BOUND	
C	C68K	Cosmic Software	COSMIC	
C	GNU-C	Free Software Foundation, Inc.	ELF/DWARF	
C	GNU-C	Free Software Foundation, Inc.	COFF	
C	GNU-C	Free Software Foundation, Inc.	ELF/DWARF	

Language	Compiler	Company	Option	Comment
C	GREEN-HILLS-C	Greenhills Software Inc.	COFF	
C	ICC68K	Introl Corporation	ICOFF	
C	MCC	Mentor Graphics Corporation	IEEE	
C	HT-68K	Microchip Technology Inc.	HITECH	
C	HICROSS-68K	NXP Semiconductors	HICROS S	
C	CC68K	NXP Semiconductors	COFF	
C	ULTRA-C	Radisys Inc.	ROF	OS/9 compilers
C	OS/9-C	Radisys Inc.	ROF	
C	CROSSCODE-C	SDSI	SDS	
C	SCC68K	Sierra	COFF	
C	SUN3-CC	Oracle Corporation	DBX	
C	ICC68K	TASKING	COFF	
C	ICC68K	TASKING	IEEE	
C	TT-68K	TASKING	IEEE	
C	TCC68K	TASKING	AOUT	only source and syms
C	TEKTRONIX-C	Tektronix	COMFOR	
C	D-CC	Wind River Systems	IEEE	
C	D-CC	Wind River Systems	ELF/DWARF	
C++	ORGANON-C++	CAD-UL ElectronicServices GmbH	BOUND	
C++	GNU-C++	Free Software Foundation, Inc.	DBX	
C++	GNU-C++	Free Software Foundation, Inc.	ELF/DWARF	
C++	CCC68K	Mentor Graphics Corporation	IEEE	

Language	Compiler	Company	Option	Comment
C++	HICROSS-68K	NXP Semiconductors	HICROS S	
C++	CODEWARRIOR	NXP Semiconductors	ELF/ DWARF	
C++	CROSSCODE-C++	SDSI	SDS	
C++	D-C++	Wind River Systems	ELF/ DWARF	
MODULA	MOD68K	Introl Corporation	ICOFF	
MODULA	MCS2	Multichannel systems GmbH	COFF	
MODULA	MCDS	NXP Semiconductors	MCDS	
PASCAL	MPC	Mentor Graphics Corporation	IEEE	
PEARL	RTOS	IEP GmbH	SYM/ LOC	no type/locals info

RTOS Support

Company	Product	Comment
Atego Ltd.	AdaWorld ARTK	
KadakProducts Ltd.	AMX	
Oracle Corporation	ChorusOS	
CMX Systems Inc.	CMX-RTX	
Synopsys, Inc	MQX	2.40 and 2.50, 3.6
	MTOS-UX	
Mentor Graphics Corporation	Nucleus PLUS	
Radisys Inc.	OS-9	
Enea OSE Systems	OSE Classic	(OS68)
Enea OSE Systems	OSE Delta	4.x and 5.x
	RealTime Craft	(XEC68k)

Company	Product	Comment
Quadros Systems Inc.	RTXC 3.2	
IBM Corp.	SDT-Cmicro	
-	uCLinux	Kernel Version 2.4 and 2.6, 3.x
Mentor Graphics Corporation	VRTX32	
Mentor Graphics Corporation	VRTXmc	
Mentor Graphics Corporation	VRTXsa	
Wind River Systems	VxWorks	5.x and 6.x

Debugger Support

CPU	Tool	Company	Host
	WINDOWS CE PLATF. BUILDER	-	Windows
	CODE::BLOCKS	-	-
	C++TEST	-	Windows
	ADENEON	-	
	X-TOOLS / X32	blue river software GmbH	Windows
	CODEWRIGHT	Borland Software Corporation	Windows
	CODE CONFIDENTIAL TOOLS	Code Confidence Ltd	Windows
	CODE CONFIDENTIAL TOOLS	Code Confidence Ltd	Linux
	EASYCODE	EASYCODE GmbH	Windows
	ECLIPSE	Eclipse Foundation, Inc	Windows
	CHRONVIEW	Inchron GmbH	Windows

CPU	Tool	Company	Host
	LDRA TOOL SUITE	LDRA Technology, Inc.	Windows
	UML DEBUGGER	LieberLieber Software GmbH	Windows
	SIMULINK	The MathWorks Inc.	Windows
	ATTOL TOOLS	MicroMax Inc.	Windows
	VISUAL BASIC INTERFACE	Microsoft Corporation	Windows
	LABVIEW	NATIONAL INSTRUMENTS Corporation	Windows
	TPT	PikeTec GmbH	Windows
	CANTATA	QA Systems Ltd	Windows
	RAPITIME	Rapita Systems Ltd.	Windows
	RHAPSODY IN MICROC	IBM Corp.	Windows
	RHAPSODY IN C++	IBM Corp.	Windows
	TESSY	Razorcat Development GmbH	Windows
	DA-C	RistancASE	Windows
	TRACEANALYZER	Symtavision GmbH	Windows
	ECU-TEST	TraceTronic GmbH	Windows
	UNDODB	Undo Software	Linux
	TA INSPECTOR	Vector	Windows
	VECTORCAST UNIT TESTING	Vector Software	Windows
	VECTORCAST CODE COVERAGE	Vector Software	Windows
68K	OS68 DEBUGGER	Enea OSE Systems	-
68K	SDT CMICRO	IBM Corp.	Windows
68K	DIAB RTA SUITE	Wind River Systems	Windows

Operation Voltage and Frequency

The maximum operation frequency of TRACE32-ICE depends on:

The max. frequency of the CPU

The access time of the overlay memory (15ns or 35ns)

- The mapper mode (Slow or Fast)
- The number of waitstates (WO = 0 waitstates W1 = 1 waitstate)
- The dual-port access mode

Denied and Request Access

Operating Voltage

This list contains information on probes available for other voltage ranges.

Probes not noted here supply an operation voltage range from 4.5V to 5.5V. tbd

Dimensions

Module Dimensions

tbd

Available Tool Chain

TRACE32 provides a complete set of development tools for this family. This includes:

- The In-Circuit Emulator TRACE32-ICE
- The high speed RISC Emulator TRACE32-FIRE
- The BDM/JTAG/ONCE etc. based In-Circuit Debugger TRACE32-ICD
- The ROM Monitor based In-Circuit Debugger TRACE32-ICD

- The ICD Trace, a trace extension to the BDM/JTAG debuggers or ROM monitors
- Evaluation boards, which can be used until the target hardware is available.
- The Instruction Set Simulator (SIM), a software tool for code test without any hardware

The following list give an overview which development tools are available for the specific derivatives of this family.

tbd

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Module Description

Detailed Order Information

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