*Emulation Memory**ICE-Base**High Speed State Analyzer**Emulation Controller Unit***System Controller Unit****System Controller 32 Bit**

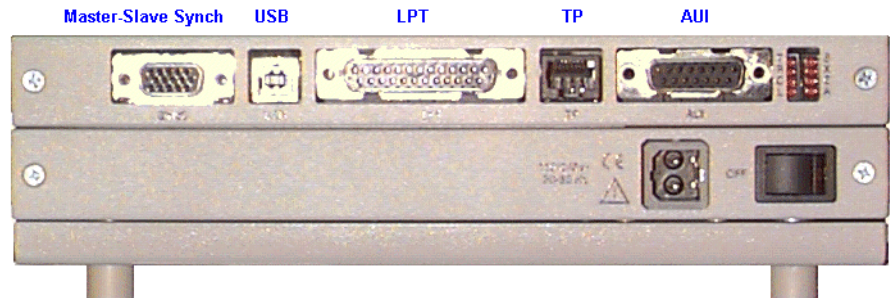
- Up to 50 MIPS
- Max. 64 MByte Memory
- Parallel Interface
- Ethernet Interface
- USB Interface
- Master-Slave Synchronisation

The heart of the TRACE32 Development System is the System Controller. It is the link between the target system and the host and therefore its performance determines the overall performance of the combined development system. The use of a 32 bit CPU and a large directly addressable memory allows the use of a fast, memory oriented software structure, which is essential for high-performance systems development. This shift of emphasis from the host to the TRACE32 results in a considerable improvement in overall system performance and also significantly reduces host system dependency. By the fast

interface to the host (fiber optic or ETHERNET), download speeds between 100 KByte/sec to 1 MByte/sec are typical. The com-

munications interface card is interchangeable so that other systems may be accommodated.

System Controller SCU32



Controller

The heart of the TRACE32 Development System is the System Controller. It is the link between the target system and the host and therefore its performance determines the overall performance of the combined development system. The use of a 32 bit CPU and a large directly addressable memory allows the use of a fast, memory oriented software structure, which is essential for high-performance systems development. This shift of emphasis from the host to the TRACE32 results in a considerable improvement in overall system performance and also significantly reduces host system dependency. By the fast interface to the

host (fiber optic or ETHERNET), download speeds between 100 KByte to 1 MByte/sec are typical. The communications interface card is interchangeable so that other systems may be accommodated.

- CPU
 - CPU with 32 bit architecture and up to 50 MIPs throughput
 - Maximum 128 Mbyte DRAM
 - Universal, CPU independent multiprocessor system bus
- Power Supply
 - 90W with short circuit and temperature protection
 - Electronic controlled fan

Parallel Interface

- Direct Connection to Printer Port on MS-DOS Computers, no Interface Card needed
- Recommended for LAPTOPs and 8 Bit Applications
- Interface
 - 100 to 500 Kbyte/s download speed
 - EPP Support
 - 3 m parallel interface cable included
 - LPT1: or LPT2: selectable

USB Interface

- 12 MBit/Sec

Ethernet Interface

- 10BaseT Connector
- AUI Connector
- Interface
 - up to 1 MByte/s download speed
 - Ethernet interface with AUI connector
 - Supports UDP, ICMP, ARP and RARP protocols

Memory Extension

- 8..64 MByte Memory Extension

Master-Slave Interface

- Up to 4 Emulators or Logic Analyzer systems can be synchronized
- Start-Stop Synchronisation
- Time-Stamp Synchronisation

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Module Description

Detailed Order Information

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